

**DOCKET NUMBER**  
70404.90/ma

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Toshihide TSUBATA et al. Application No.: 10/595,640 Filing or 371(c) Date: Title: TRANSISTOR AND CVD APPARATUS USED TO DEPOSIT GATE INSULATING FILM THEREOF	Art Unit: 2811  Examiner: Unknown
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**COMMENTARY RE INFORMATION DISCLOSURE STATEMENT**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, submitted herewith is a copy of an Official Communication issued in a corresponding Japanese patent application and a copy of one reference cited therein. Applicants do not submit herewith a copy of JP 09-289210 as the reference was cited in an Information Disclosure Statement previously filed with the U.S. Patent Office on May 2, 2006. Applicants also submit one additional reference.

For the Examiner's convenience, we have enclosed a completed Form PTO/SB/08a.

Applicants respectfully request that the cited references be made of record in the subject application.

Respectfully submitted,

Date: October 3, 2006

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